



### FTS-R21G-S31L-002D

Multirate SFP 125Mbps-2.67Gbps, 1310nm, single-mode, 2km



## Description

FTS-R27G-S31L-002D series SFP transceiver can be used to setup a reliable, high speed serial data link over single-mode fiber. Maximum link span can reach 2km. Module is fully compliant with SFP MSA specifications and it is available in two hardware versions

Model	Operating case temperature
FTS-R27G-S31L-002D	0~70°C
FTS-R27G-S31L-002DI	-40~85°C

Host device can access module internal EEPROM memory and DDMI via I<sup>2</sup>C interface.

DDMI (Digital Diagnostic Monitoring Interface) is fully compliant with DOM. Transceiver can be prepared as compatible with: Cisco, HP, Netgear, Avaya, D-Link, Brocade, Extreme Networks, Huawei, Enterasys, 3Com, Alcatel-Lucent and other. To confirm compatibility please contact technical support before ordering.

## **Applications**

- Gigabit Ethernet (1.25Gbps)
- Fiber Channel (2.125Gbps)
- SONET OC-3,OC-12,OC-24,OC-48 and SDH STM 1,4,16
- OTN

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## **Key features**

- LC Duplex receptacle
- Transmission distance up to 2km\*
- Fabry-Perot laser diode 1310nm transmitter, PIN receiver
- Throughput up to 2.67Gb/s
- Fully compliant with SFP MSA INF-8074i rev. 1.0 and INF-8472 rev. 11.3
- Hot-Pluggable
- RoHS compliant
- Class 1 laser safety
- Low power dissipation (<1W)
- Metal case for low EMI
- Operating case temperature\* : 0~70°C / -40~85°C

#### **Specification**

Supported transmission technology	Output power
Gigabit Ethernet, Fibre Channel	_9~-3dBm
Speed supported for Ethernet technology	Receiver sensitivity
<u>125Mbps – 2.67Gbps</u>	-18dBm (at 2.67Gbps)
Speed supported for Fibre Channel technology	Power supply voltage
2.125Gbps, 1.0625Gbps	3.3V
Transmission medium	Total power consumption
Single-mode fiber 9/125µm	< 1W
Transmission distance**	Operating environment – temperature*
<u>2km</u>	<u>0~70°C / -40~+85°C</u>
Receptacle type	Operating environment - humidity
LC Duplex	5~95% non-condensing
Wavelength	Dimensions
1310nm	55.6mm × 13.4mm × 8.5mm

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<sup>\* -</sup> standard / industrial version

<sup>\*\* -</sup> transmission distance depends on optical link attenuation





# **Detailed technical specification**

#### Pin Description

Pin	Name	Function/Description	Engagement order	Notes
1	VeeT	Transmitter Ground	1	-
2	TX Fault	Transmitter Fault Indication	3	1
3	TX Disable	Transmitter Disable-Module disables on high or open	3	2
4	MOD-DEF2	Module Definition 2-Two wire serial ID interface	3	3
5	MOD-DEF1	Module Definition 1-Two wire serial ID interface	3	3
6	MOD-DEF0	Module Definition 0-Grounded in module	3	3
7	Rate Select	Not Connected	3	
8	LOS	Loss of Signal	3	4
9	VeeR	Receiver Ground	1	-
10	VeeR	Receiver Ground	1	-
11	VeeR	Receiver Ground	1	-
12	RD-	Inverse Received Data out	3	5
13	RD+	Received Data out	3	5
14	VeeR	Receiver Ground	1	-
15	VccR	Receiver Power - +3.3V±5%	2	6
16	VccT	Transmitter Power - +3.3 V±5%	2	6
17	VeeT	Transmitter Ground	1	-
18	TD+	Transmitter Data In	3	7
19	TD-	Inverse Transmitter Data In	3	7
20	VeeT	Transmitter Ground	1	-

#### Notes:

- 1. TX Fault is open collector/drain output which should be pulled up externally with a  $4.7K\Omega-10K\Omega$  resistor on the host board. When high, this output indicates a laser fault of some kind. Low indicates normal operation.
- 2. TX Disable input is used to shut down the laser output per the state table below. It is pulled up to Vcc within the module.

Low (0 - 0.8V): Transmitter on

Open or High (2.0 – VccT): Transmitter Disabled

- 3. Mod-Def 0, 1, 2. These are the module definition pins. They should be pulled up to Vcc with a  $4.7K\Omega-10K\Omega$  resistor on the host board Mod-Def 0 is grounded by the module to indicate that the module is present.
  - Mod-Def 1 is clock line of two wire serial interface for optional serial ID.
  - Mod-Def 2 is data line of two wire serial interface for optional serial ID.
- 4. LOS (Loss of signal) when high, this output indicates the received optical power is below the worst case receiver sensitivity (as defined by the standard in use). Low indicates normal operation.
- 5. These are the differential receiver outputs. They are AC coupled  $100\Omega$  differential lines which should be terminated with  $100\Omega$  differential at the user SERDES. The AC coupling is done inside the module and thus not required on the host board.
- 6. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V±5% at the SFP connector pin. The in-rush current will typically be no more than 30mA above steady state supply current after 500ns.
- 7. TD-/+: These are the differential transmitter inputs. They are AC coupled differential lines with  $100\Omega$  differential termination inside the module. The AC coupling is done inside the module and is thus not required on host board.

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## **Electrical parameters**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Transmitter Differential Input Volt	+/-TX_DAT	300		2200	mV p-p	1
Receiver Differential Output Volt	+/-RX_DAT	600		1400	mV p-p	2
Tx_Disable Input Voltage – Low	VIL	0		8.0	V	
Tx_Disable Input Voltage – High	ViH	2.0		Vcc	V	
Tx_Fault Output Voltage – Low	V <sub>OL</sub>	0		8.0	V	3
Tx_Fault Output Voltage – High	V <sub>OH</sub>	2.0		Vcc	V	3
Rx_LOS Output Voltage- Low	$V_{OL}$	0		8.0	V	3
Rx_LOS Output Voltage- High	V <sub>OH</sub>	2.0		Vcc	V	3
Total current requirement				300	mA	

### Transmitter parameters

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Central wavelength	λς	1270	1310	1360	nm	
Spectral width	Δλ			4	nm	
Launch optical power	P <sub>o</sub>	-9		-3	dBm	4
Extinction ratio	EX	5			dB	
Optical rise/fall time	T <sub>rise</sub> /T <sub>fall</sub>			260	ps	5
Eye diagram	Compliant with requirements					

#### Receiver parameters

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Sensitivity	P <sub>min</sub>			-19	dBm	6,8
	Fmin			-18	UDIII	7
Central wavelength	λс	1260		1360	nm	
Receiver overload	P <sub>MAX</sub>	-3			dBm	6
RX_LOS Asserted	SA	-35			dBm	
RX_LOS De-Asserted	SD			-20	dBm	
RX_LOS Hysteresis	-		3.0		dB	
Optical return loss	ORL	27			dB	

#### Notes:

- Internally AC coupled and terminated to  $100\Omega$  differential load.
- Internally AC coupled, but requires a  $100\Omega$  differential termination or internal to Serializer/Deserializer.
- It is open collector/drain output which should be pulled up externally to Vcc with a  $4.7K\Omega-10K\Omega$  resistor on the host board. LOS: logic 0 indicates normal operation; logic 1 indicates no signal detected.
- Optical power is launched into SMF
- 5. 20-80%
- 6. Measured with PRBS 2<sup>31</sup>-1 at 10<sup>-12</sup> BER
- At 2.67Gbps speed 7.
- At 1.25Gbps speed

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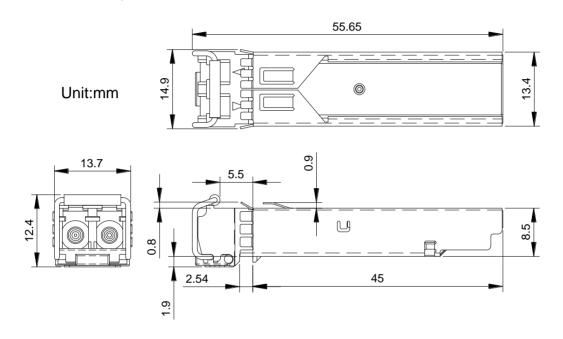
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## Mechanical specification



# **Recommended environment conditions**

Parameter Parameter	Symbol	Min	Тур	Max	Unit
Operating Temperature Range (industrial)	T	-40	-	85	0C
Operating Temperature Range (standard)	T	0	25	70	0C
Supply Voltage	$V_{CC}$	3.135	3.3	3.465	V
Relative Humidity	RH	5	-	95	%

# **Ordering information**

FTS-R27G-S31L-002**D**- 1310nm, 2km, single-mode, LC duplex, **DDMI**, commercial temperature (0~70°C) FTS-R27G-S31L-002**DI**- 1310nm, 2km, single-mode, LC duplex, **DDMI**, **extended temperature** (-40~85°C)

For further information regarding host device PCB layout recommendation, power supply requirements, EEPROM memory map, DDMI specification please check:

SFF-8472 - Description of EEPROM and Digital Diagnostic Monitoring Interface and INF-8074 - Technical specification for SFP transceiver

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